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Amend A
UP/Revised
12/13/12

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Douglas Elmer Wallace, Jr.; Stephanus Saputro
Assignee: Dell Products, L.P.
Title: Printed Circuit Suppression of High-Frequency Spurious Signals
Serial No.: 09/931,229 Filing Date: August 16, 2001
Examiner: Justin P. Bettendorf Group Art Unit: 2817
Docket No.: DC-02864

Austin, Texas
December 3, 2002

COMMISSIONER FOR PATENTS
Washington, D. C. 20231

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TECHNOLOGY CENTER 2800

RESPONSE TO NON-FINAL OFFICE ACTION

Dear Sir:

This paper is responsive to the Office action dated September 3, 2002, having a shortened statutory period expiring December 3, 2002. Further examination and reconsideration are respectfully requested in view of the amendments and remarks set forth below.

AMENDMENTS

In the Specification

In accordance with 37 CFR § 1.121(b)(1)(iii), Attachment B contains marked up versions of the replacement paragraphs illustrating the newly introduced changes in the specification.

Please replace the paragraph beginning on Page 8, line 13 with the following amended paragraph:

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Also connected to pad 20, and therefore to B⁺, is a serpentine conductor 30 that may, as are pads 20 and 21, be printed on the top surface of PCB 100. As may be seen in Figure 2, serpentine conductor 30 comprises an originating segment 31 connected to pad 20 and a terminating segment 32 coupled to a planar conductor 40. (The relevance of conductor 40 will be revealed below.) Conductor 30 also comprises a number of substantially linear segments 34a,